

Certification Course
on
Circuit Design using MULTISIM

Co ordinator: Miss.A.Jyothirmayi

Date(s) of Event : 21/09/2020 to 09/10/2020

Organizing department:

Electrical and Electronics Engineering



K.S.R.M.COLLEGE OF ENGINEERING

(UGC-AUTONOMOUS)

Kadapa,Andhra Pradesh,India-516 005

Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu.

AnISO14001:2004&9001:2015CertifiedInstitution

Cr./KSRMCE/(Department of EEE)/2020-2021

Date: 16/09/2020

To

The Principal,

KSRM College of Engineering,

Kadapa.

Respected Sir

Sub: KSRMCE-(Department of EEE) permission to conduct certification course on "Circuit Design using MULTISIM"-Request-Regd.

It is brought to your kind notice that, with reference to the cited, the EEE department is planning to conduct Certification Course on "Circuit Design using MULTISIM" for B.Tech, V Sem Students from 21 September ,2020 to 09 October ,2020.In this regard I kindly request you to grant permission to conduct the certification course. This is submitted for your kind perusal.

Thanking you sir,

Yours Faithfully

Miss.A.Jyothirmayi

Asst.Prof, Dept.EEE

KSRMCE, Kadapa.

*Forwarded to
Principal Sir*

Jyothirmayi
HEAD
Department of Electrical &
Electronics Engineering
K.S.R.M. College of Engineering
Guddapah - 516 003

Permitted
K.S.R.M. COLLEGE OF ENGINEERING
KADAPA - 516 003 (A.P.)
PRINCIPAL

To the Director for Information

To All Deans/HoD's/IQAC



**K.S.R.M.COLLEGE OF ENGINEERING
(UGC-AUTONOMOUS)**

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Cr./KSRMCE/(Department of EEE)/2020-2021

Date: 16/09/2020

Circular

All the B.Tech III Sem EEE students are here by informed that department of EEE is going to conduct certificate course on "Circuit Design using MULTISIM" for B.Tech, V Sem Students from 21 September ,2020 to 09 october ,2020. Interested students may register their names on or before 19 September,2020 before 5 Pm.

For any queries contact faculty coordinator :

Miss.A.Jyothirmayi, Asst.Prof, Dept.EEE, KSRMCE, Kadapa.

Amy
HEAD
Principal
Department of Electrical &
Electronics Engineering
K.S.R.M. College of Engineering
Cuddapah - 516 003



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Department of Electrical and Electronics Engineering

Certification Course


On

Circuits Design using MULTISIM

List of Participants

S.No	Roll Number	Name of the Student
1	189Y1A0202	BANDELADEVA PAVAN KARUNYA
2	189Y1A0203	BOLLAVARAM PHANI KRISHNA
3	189Y1A0205	CHALLA MANASA (W)
4	189Y1A0206	CHALLA SHIVA TEJA REDDY (W)
5	189Y1A0207	CHEVULA SAMPATH KUMAR
6	189Y1A0208	DANDU BALA SAI
7	189Y1A0209	GANGAVARAMGANESHKUMARREDDY
8	189Y1A0210	GUBILI NAVEEN KUMAR
9	189Y1A0211	GURAI AHGARI PAVAN KALYAN
10	189Y1A0212	HASANAPURAMCHARANPRAKASH
11	189Y1A0214	JANDYALA NAGA BHASKAR
12	189Y1A0215	KADIRI PARAMESWAR REDDY
13	189Y1A0216	KALISSETTY SURENDRA MARUTHI
14	189Y1A0217	KAMISSETTY VAMSI
15	189Y1A0218	KANIKE SRINIVASULU
16	189Y1A0219	KARNATI SAI SIVANANDA REDDY
17	189Y1A0220	KOKKANTI ROHITH
18	189Y1A0221	KOMMA PEDDI REDDY
19	189Y1A0222	KONANAVANI (W)
20	189Y1A0223	KONDA SREENIVASA RAO
21	189Y1A0224	KONDREDDY MANJU BHARGAVI (W)
22	189Y1A0225	K.VEERA CHANDRA LIKHITA (W)
23	189Y1A0226	KUKKALAREDDY HEMANTH REDDY
24	189Y1A0227	KUKKALAREDDY SUMANTH REDDY
25	189Y1A0228	MACHA HARSHITH
26	189Y1A0229	MANJULA AKANKSHA (W)

27	189Y1A0232	MUGOLLA GANGAPRASANTH
28	189Y1A0234	NUKALA ARUNA (W)
29	189Y1A0235	PAGADALA PRIYANKA (W)
30	189Y1A0236	PERAM PAVANI (W)
31	189Y1A0237	PULIMADYALA MOHAMMED SADAK
32	189Y1A0238	PUTLURU BHARATH KUMAR REDDY
33	189Y1A0239	RAVULA UPENDRA
34	189Y1A0241	SAMBU KEERTHI (W)
35	189Y1A0242	SANIVARAPURAMAKRISHNAREDDY
36	189Y1A0243	SHAIK AISHA (W)
37	189Y1A0244	SHAIK KHALEEFA
38	189Y1A0245	SHAIK MULLA KHAJA MOINUDDIN
39	189Y1A0246	SHAIK NAZEER BASHA
40	189Y1A0247	SHAIK YOUSUF
41	189Y1A0250	UPPALAPATI SURENDRA BABU
42	189Y1A0251	VADDEMANI PAVAN KUMAR REDDY
43	189Y1A0252	VEMA VENKATESH
44	189Y1A0253	VEMA YOGESWARA
45	199Y5A0201	BELLAGANTI DIVYASWINI (W)
46	199Y5A0202	CHINTHAKUNTA GAYATHRI (W)
47	199Y5A0203	GORANTLA BHUPATHI RAJU
48	199Y5A0204	GUNDI NAGANNA
49	199Y5A0205	KALAMALLA KALANDAR
50	199Y5A0206	KAMMARA UDAYA SREE (W)
51	199Y5A0213	YAMAVARAM SWAPNA (W)



Coordinator



Department of Electrical &
Electronics Engineering
K.S.R.M. College of Engineering
Cuddapah - 516 003

Design of Electrical Circuits Using MULTISIM

Syllabus of Course

Sl. No.	Topic	Hours
		Theory
Module 1	Introduction to MULTISIM software ,Design procedur and Steps, Evalaution of voltage/ Currents using kvl, Evalaution of voltage/ Currents using kvl	07
Module 2	Evalaution of voltage/ Currents using kcl ,Design of dependent Source Circuits for dc excitation,	08
Module 3	, Design of dependent Source Circuits for ac excitation Design of dependent Source Circuits for dc excitation, Design of dependent Source Circuits for ac excitation	08
Module 4	Response of half wav e and full wave rectifier circuits, Voltage divider circuit designing, Design of Bridge rectifier circuit	07

Text Books

- 1.Fundamentals of Electronic Circuit Design: Getting Started: MultiSim- John Wiley & Sons;
- 2.NI MULTISIM user manual-<file:///C:/Users/Admin/Desktop/CC/NI%20MULTISIM.pdf>

Agarwal
HEAD
Department of Electrical &
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K.S.R.M. College of Engineering
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Department of Electrical and Electronics Engineering

Certification Course

On

Circuit Design using MULTISIM

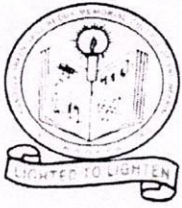
Schedule

Timing: 4:00pm – 6:00pm

S.No	Date	Course Coordinator	Topic Coverd
1	21/09/2020	Smt.SalehaTabassum	Introduction to MUTISIM software
2	22/09/2020	Smt.SalehaTabassum	Design procedur and Steps
3	23/09/2020	Smt.SalehaTabassum	Evalaution of voltage/ Currents using kvl
4	24/09/2020	Miss.A.Jyothirmayi	Evalaution of voltage/ Currents using kvl
5	25/09/2020	Miss.A.Jyothirmayi	Evalaution of voltage/ Currents using kcl
6	26/09/2020	Miss.A.Jyothirmayi	Design of dependent Source Circuits for dc excitation
7	28/09/2020	Smt.SalehaTabassum	Design of dependent Source Circuits for dc excitation
8	29/09/2020	Smt.SalehaTabassum	Design of dependent Source Circuits for ac excitation
9	30/09/2020	Smt.SalehaTabassum	Design of dependent Source Circuits for ac excitation
10	01/10/2020	Miss.A.Jyothirmayi	Response of half wavw and full wave rectifier circuits.
11	05/10/2020	Miss.A.Jyothirmayi	Voltage divider circuit designing
12	06/10/2020	Miss.A.Jyothirmayi	Design of Bridge rectifier circuit
13	07/10/2020	Smt.SalehaTabassum	Designing of LED glow circuit.
14	08/10/2020	Smt.SalehaTabassum	Designing of Automatic Door Lock System
15	09/10/2020	Miss.A.Jyothirmayi	Amplification of frequency signal using MATALB/Simulink.


Coordinator


HEAD
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Kadapa - 516 003



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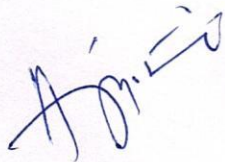
Department of Electrical and Electronics Engineering

Activity Report


Name of the Event : Certification Course Circuits Design using ULTISIM
Date(s) of the Event : 21/09/2020 to 09/10/2020
Scheduled Time : 4.00 to 6.00PM
Target Audience : B.Tech V Sem Students
Student Co-ordinator : SHAIK YOUSUF, V Sem, EEE
Venue of the Event. : Online (<https://meet.google.com/lookup/hjomysvt74>)

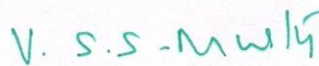
Activity Description:


MULTISIM is one of the important tools for electrical students to learn the circuit designing. Department of EEE organized a certification course on "Circuits Design using MULTISIM". Head of the Department, faculty & participants inaugurated with all good spirit. Resource persons began with the Introduction to MULTISIM followed by Measurement of electrical parameters. Implementation of Network Solving Techniques; Sinusoidal Network Analysis. Application of Electrical Concepts followed by finally valedictory. Students were issued participation certificates by the Head of the Department.



Coordinator


HEAD
Department of Electrical &
Electronics Engineering
HOD
K.S.R.M. College of Engineering
Cuddapan - 516 003


Principal
PRINCIPAL
K.S.R.M. COLLEGE OF ENGINEERING
KADAPA - 516 003. (A.P.)

 (45)



235-POTHURAJU SAI... >



237-Thirumala >



238-SAGGAM SREEKANTH >



239- Sale Ramu >



204_BANDI NEERAJA >



205- BERI YASWANTH >




206 CHEMIKALA RAMA... >



209-DUGGIREDDY >



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Kadapa, Andhra Pradesh, India- 516 003

Certificate Course on

“ Design of Electrical Circuits using MULTISIM ”

24/08/2020- 11/09/2020

Organized by

DEPARTMENT OF
ELECTRICAL AND ELECTRONICS ENGINEERING



K.S.R.M.COLLEGE OF ENGINEERING

(UGC-AUTONOMOUS)

Kadapa, Andhra Pradesh, India-516 005

Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu.

Department of Electrical and Electronics Engineering

Certification Course on MATLAB Programming

Attendance Sheet


04:00 to 05:00 PM

S.No	Name of the Student	Roll List	21/9	22/9	23/9	24/9	26/9	28/9	29/9	30/9	1/10	5/10	6/10	7/10	8/10	9/10	
1	ANKIREDDYPALLI BALAKONDA REDDY	189Y1A0201	/	/	/	/	/	/	/	/	/	/	/	A	/	/	
2	BANDELADEVA PAVAN KARUNYA	189Y1A0202	/	/	A	/	/	/	/	/	A	/	/	/	/	/	
3	BOLLAVARAM PHANI KRISHNA	189Y1A0203	/	/	/	/	/	/	/	A	/	/	/	/	/	/	
4	CHALLA MANASA (W)	189Y1A0205	/	/	/	/	/	/	A	/	/	/	A	/	/	/	
5	CHALLA SHIVA TEJA REDDY (W)	189Y1A0206	A	/	/	/	/	/	/	/	/	/	/	/	/	/	
6	CHEVULA SAMPATH KUMAR	189Y1A0207	/	/	/	/	/	/	/	A	/	/	/	/	/	/	
7	DANDU BALA SAI	189Y1A0208	/	/	/	A	/	/	/	/	/	/	/	/	/	A	
8	GANGAVARAMGANESHKUMAR REDDY	189Y1A0209	/	/	/	/	/	/	/	/	/	/	/	/	/	/	
9	GUBILI NAVEEN KUMAR	189Y1A0210	/	/	/	/	/	/	/	/	/	/	/	/	/	/	
10	GURAI AHGARI PAVAN KALYAN	189Y1A0211	A	/	/	/	/	/	/	/	/	/	/	A	/	/	
11	JANDYALA NAGA BHASKAR	189Y1A0214	/	/	/	/	A	/	/	/	/	/	/	/	/	/	
12	KADIRI PARAMESWAR REDDY	189Y1A0215	/	/	/	/	/	A	/	/	A	/	/	/	/	/	
13	KALISSETTY SURENDRA MARUTHI	189Y1A0216	/	A	/	/	/	/	/	A	/	/	/	/	/	/	

14	KAMISSETTY VAMSI	189Y1A0217	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
15	KANIKE SRINIVASULU	189Y1A0218	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
16	KARNATI SAI SIVANANDA REDDY	189Y1A0219	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓
17	KOKKANTI ROHITH	189Y1A0220	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
18	KOMMA PEDDI REDDY	189Y1A0221	A	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓
19	KONANAVANI (W)	189Y1A0222	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
20	KONDA SREENIVASA RAO	189Y1A0223	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
21	KONDREDDY MANJU BHARGAVI (W)	189Y1A0224	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
22	KORAPALA VEERA CHANDRA LIKHITA (W)	189Y1A0225	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
23	KUKKALAREDDY HEMANTH REDDY	189Y1A0226	✓	A	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓
24	KUKKALAREDDY SUMANTH REDDY	189Y1A0227	✓	✓	✓	A	✓	✓	✓	✓	✓	A	✓	✓	✓	A	✓
25	MIMME SREENATH	189Y1A0231	✓	✓	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓
26	MUGOLLA GANGAPRASANTH	189Y1A0232	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
27	MUPPURI GIRIKUMAR	189Y1A0233	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
28	NUKALA ARUNA (W)	189Y1A0234	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓
29	PAGADALA PRIYANKA (W)	189Y1A0235	✓	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓
30	PERAM PAVANI (W)	189Y1A0236	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
31	PULIMADYALA MOHAMMED SADAK	189Y1A0237	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
32	PUTLURU BHARATH KUMAR REDDY	189Y1A0238	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	A	✓
33	RAVULA UPENDRA	189Y1A0239	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
34	SAMBU KEERTHI (W)	189Y1A0241	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
35	SANIVARAPURAMAKRISHNAREDDY	189Y1A0242	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
36	SHAIK AISHA (W)	189Y1A0243	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
37	SHAIK KHALEEFA	189Y1A0244	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	A
38	SHAIK MULLA KHAJA MOINUDDIN	189Y1A0245	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	A	✓

39	SHAIK NAZEER BASHA	189Y1A0246	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓		
40	SHAIK YOUSUF	189Y1A0247	✓	✓	✓	A	✓	✓	✓	A	✓	✓	✓	✓	✓	A	
41	UPPALAPATI SURENDRA BABU	189Y1A0250	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
42	VADDEMANI PAVAN KUMAR REDDY	189Y1A0251	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
43	VEMA VENKATESH	189Y1A0252	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	A	
44	VEMA YOGESWARA	189Y1A0253	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
45	BELLAGANTI DIVYASWINI (W)	199Y5A0201	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
46	CHINTHAKUNTA GAYATHRI (W)	199Y5A0202	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	
47	GORANTLA BHUPATHI RAJU	199Y5A0203	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
48	GUNDI NAGANNA	199Y5A0204	✓	A	✓	✓	✓	✓	A	✓	✓	A	✓	✓	✓	✓	
49	KALAMALLA KALANDAR	199Y5A0205	✓	✓	✓	✓	✓	✓	✓	✓	A	✓	✓	✓	✓	✓	
50	KAMMARA UDAYA SREE (W)	199Y5A0206	✓	✓	✓	A	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
51	MODAPOTHULA ANIL	199Y5A0207	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	


Coordinator


HEAD
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Cuddapah - 516 003

How can a student get Multisim?

- The student must purchase the software online through one of **our distributors like Studica**.
- Students can also download an evaluation version of the NI Circuit Design Suite Education Edition (which includes all features from the Student Edition) from our website.

How important is Multisim?

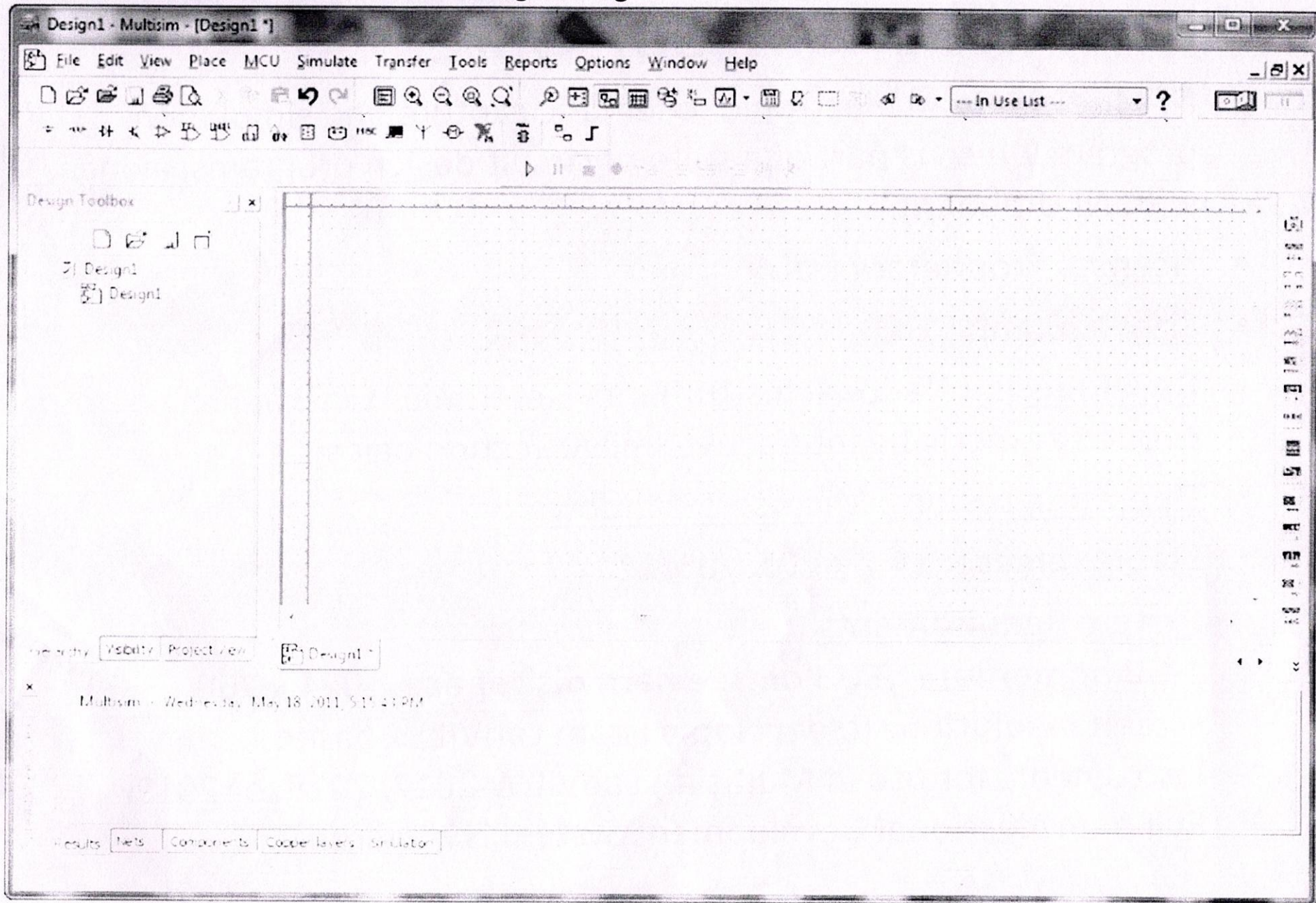
Multisim has an **intuitive interface that helps educators reinforce circuit theory and improve retention of theory throughout engineering curriculum.** Researchers and designers use Multisim to reduce PCB prototype iterations and save development costs by adding powerful circuit simulation and analyses to the design flow.

- Multisim is one among the 10 must learn Electrical Engineering Software's
- The job of an Electrical Engineer involves the design, development, simulation, prototyping, and testing of electrical equipment and systems
- Here's given a list of electrical engineering software every Electrical graduate must learn.

10 Must Learn Electrical Engineering Software

1. MATLAB (Software for Numerical Computing)
2. Simulink (GUI based software for Dynamic System Simulation)
3. Pspice (Electrical Schematic Software)
4. Multisim (Circuit Simulation & PCB Design Software)
- 5.
6. Power World Simulator (Visual Electrical Engineering Software software)
7. PSCAD (Electromagnetic Transient Analysis Software)
8. PSS/E (An Electrical Engineering Software for Power System Simulations)
9. LabVIEW (Designing Interfacing and HMIs)
10. Kiel u Vision







Department of Electrical & Electronics Engineering



About MULTISIM Software






- NI Multisim is an electronic schematic capture and simulation program which is part of a suite of circuit design programs, along with NI Ultiboard.
- **License:** Proprietary EULA
- **Size:** ~260mb
- **Developer(s):** National Instruments Electronics Workbench Group (formerly by Interactive Image Technologies)
- **Operating system:** Microsoft Windows
- **Stable release:** 14.2 / 2019/05/12
- **System Requirements**
1 GB of memory. 2 GB of free hard disk space. 1024 x 768 screen resolution. To develop custom LabVIEW-based instruments for use in Multisim, LabVIEW 2017, 2018, or 2019 Full or Professional Development System is required.

Department of Electrical & Electronics Engineering

	Print Preview button. Previews the circuit as it will be printed.
	Cut button. Removes the selected elements and places them on the Windows clipboard.
	Copy button. Copies the selected elements and places them on the Windows clipboard.
	Paste button. Inserts the contents of the Windows clipboard at the cursor location.
	Undo button. Undoes the most recently performed action.
	Redo button. Redoes the most recently performed undo.







Standard Toolbar

The Standard toolbar contains buttons for commonly performed functions. Its buttons are described below.

Button	Description
	New button. Creates a new circuit file.
	Open button. Opens an existing circuit file.
	Open Sample button. Opens a folder containing sample and getting started files.
	Save button. Saves the active circuit.
	Print Circuit button. Prints the active circuit.





Components Toolbar

The buttons in the **Components** toolbar are described below. Each button will launch the place component browser (**Select a Component** browser) with the group specified on the button pre-selected. Refer to the *Using the Place Component Browser* section for more information.

Button	Description
	Place Source button. Selects the Source components group in the browser.
	Place Basic button. Selects the Basic components group in the browser.
	Place Diode button. Selects the Diode components group in the browser.
	Place Transistor button. Selects the Transistor components group in the browser.
	Place Analog button. Selects the Analog components group in the browser.
	Place TTL button. Selects the TTL components group in the browser.

Simulation Toolbar

The Simulation toolbar contains buttons used during simulation.






Button	Description
	Run/resume simulation button. Starts/resumes simulation of the active circuit. Refer to the <i>Start/Stop/Pause Simulation</i> section of Chapter 8, <i>Simulation</i> , for more information.
	Pause simulation button. Pauses simulation. Refer to the <i>Start/Stop/Pause Simulation</i> section of Chapter 8, <i>Simulation</i> , for more information.
	Stop simulation button. Stops the simulation. Refer to the <i>Start/Stop/Pause Simulation</i> section of Chapter 8, <i>Simulation</i> , for more information.
	Pause at Next MCU Instruction Boundary button. Refer to the <i>Stepping and Breaking</i> section for more information.






Department of Electrical & Electronics Engineering

Instruments Toolbar

The buttons in the **Instruments** toolbar are described below. In each case, the button places a specific instrument on the workspace.

Some versions of Multisim do not include all of the instruments described below.

Button	Description
	Multimeter button. Places a multimeter on the workspace. Refer to the <i>Multimeter</i> section of Chapter 9, <i>Instruments</i> , for more information.
	Function Generator button. Places a function generator on the workspace. Refer to the <i>Function Generator</i> section of Chapter 9, <i>Instruments</i> , for more information.
	Wattmeter button. Places a wattmeter on the workspace. Refer to the <i>Wattmeter</i> section of Chapter 9, <i>Instruments</i> , for more information.
	Oscilloscope button. Places an oscilloscope on the workspace. Refer to the <i>Oscilloscope</i> section of Chapter 9, <i>Instruments</i> , for more information.
	Four Channel Oscilloscope button. Places a four-channel oscilloscope on the workspace. Refer to the <i>Four-Channel Oscilloscope</i> section of Chapter 9, <i>Instruments</i> , for more information.

	Place CMOS button. Selects the CMOS component group in the browser.
	Place Miscellaneous Digital button. Selects the Miscellaneous Digital component group in the browser.
	Place Mixed button. Selects the Mixed component group in the browser.
	Place Power Components button. Selects the Power component group in the browser.
	Place Indicator button. Selects the Indicator component group in the browser.



K.S.R.M. COLLEGE OF ENGINEERING

UGC - AUTONOMOUS
KADAPA, AP - 516 005

Certificate of Completion

This is to certify that

Mr./Ms. C. Manasa

Bearing the Roll No 18941A0204

has Successfully completed certification course on

Circuits Design using MULTISIM

*From 21/09/2020 to 09/10/2020, Organized by Department of
Electrical & Electronics Engineering.*

[Signature]

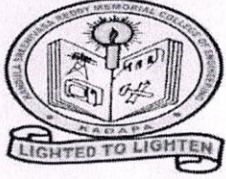
Coordinator

[Signature]

Head Of Department

[Signature]

Principal



K.S.R.M. COLLEGE OF ENGINEERING
(UGC-AUTONOMOUS)

Kadapa, Andhra Pradesh, India-516 005

Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu.

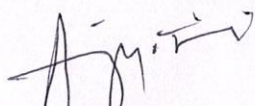
Department of Electrical and Electronics Engineering
Certification Course on Circuit Design Using MULTISIM

S.No	Name of the Student	Roll List	Is the Course content meet your expectation	Is the lecture sequence well planned	Is the level of course high	Is the course exposed you to the new knowledge and practices	Rate the Knowledge of the Speaker	Rate the value of Course in increasing your skills	Any Issues
1	ANKIREDDYPALLI BALAKONDA REDDY	189Y1A0201	yes	Agree	Strongly Agree	Yes	4	5	Nil
2	BANDELADEVA PAVAN KARUNYA	189Y1A0202	yes	Agree	Agree	Yes	5	5	Need extra Explanation
3	BOLLAVARAM PHANI KRISHNA	189Y1A0203	yes	Agree	Agree	Yes	4	5	Nil
4	CHALLA MANASA (W)	189Y1A0205	yes	Agree	Agree	Yes	5	5	Nil
5	CHALLA SHIVA TEJA REDDY (W)	189Y1A0206	yes	Agree	Agree	Yes	5	5	Provide PPT
6	CHEVULA SAMPATH KUMAR	189Y1A0207	yes	Agree	Agree	Yes	5	4	Nil
7	DANDU BALA SAI	189Y1A0208	yes	Agree	Agree	Yes	5	4	Nil
8	GANGAVARAMGANE SHKUMARREDDY	189Y1A0209	yes	Agree	Agree	Yes	4	5	Nil


9	GUBILI NAVEEN KUMAR	189Y1A0210	yes	Agree	Agree	Yes	5	5	Nil
10	GURAIHGARI PAVAN KALYAN	189Y1A0211	yes	Agree	Agree	Yes	5	5	Nil
11	JANDYALA NAGA BHASKAR	189Y1A0214	yes	Agree	Agree	Yes	5	5	Nil
12	KADIRI PARAMESWAR REDDY	189Y1A0215	yes	Agree	Agree	Yes	5	5	Nil
13	KALISSETTY SURENDRA MARUTHI	189Y1A0216	yes	Agree	Agree	Yes		5	Nil
14	KAMISSETTY VAMSI	189Y1A0217	yes	Agree	Agree	Yes	5	5	Nil
15	KANIKE SRINIVASULU	189Y1A0218	yes	Agree	Agree	Yes	5	5	Nil
16	KARNATI SAI SIVANANDA REDDY	189Y1A0219	yes	Agree	Agree	Yes	5	5	Nil
17	KOKKANTI ROHITH	189Y1A0220	yes	Agree	Agree	Yes	5	5	Nil
18	KOMMA PEDDI REDDY	189Y1A0221	yes	Agree	Agree	Yes	4	5	Nil
19	KONANAVANI (W)	189Y1A0222	yes	Agree	Agree	Yes	5	5	Nil
20	KONDA SREENIVASA RAO	189Y1A0223	yes	Agree	Agree	Yes	5	5	Nil
21	KONDREDDY MANJU BHARGAVI (W)	189Y1A0224	yes	Agree	Agree	Yes	5	5	Nil
22	KORAPALA VEERA CHANDRA LIKHITA (W)	189Y1A0225	yes	Agree	Agree	Yes	5	5	Nil

23	KUKKALAREDDY HEMANTH REDDY	189Y1A0226	yes	Agree	Agree	Yes		5	Nil
24	KUKKALAREDDY SUMANTH REDDY	189Y1A0227	yes	Agree	Agree	Yes	5	5	Nil
25	MIMME SREENATH	189Y1A0231	yes	Agree	Agree	Yes	5	5	Nil
26	MUGOLLA GANGAPRASANTH	189Y1A0232	yes	Agree	Agree	Yes	5	5	Nil
27	MUPPURI GIRIKUMAR	189Y1A0233	yes	Agree	Agree	Yes	5	5	Nil
28	NUKALA ARUNA (W)	189Y1A0234	yes	Agree	Agree	Yes		5	Nil
29	PAGADALA PRIYANKA (W)	189Y1A0235	yes	Agree	Agree	Yes	5	5	Nil
30	PERAM PAVANI (W)	189Y1A0236	yes	Agree	Agree	Yes	5	5	Nil
31	PULIMADYALA MOHAMMED SADAK	189Y1A0237	yes	Agree	Agree	Yes	5	5	Nil
32	PUTLURU BHARATH KUMAR REDDY	189Y1A0238	yes	Agree	Agree	Yes	5	5	Nil
33	RAVULA UPENDRA	189Y1A0239	yes	Agree	Agree	Yes	4	5	Nil
34	SAMBU KEERTHI (W)	189Y1A0241	yes	Agree	Agree	Yes	5	5	Nil
35	SANIVARAPURAMAK RISHNAREDDY	189Y1A0242	yes	Agree	Agree	Yes	5	5	Nil
36	SHAIK AISHA (W)	189Y1A0243	yes	Agree	Agree	Yes	5	5	Nil
37	SHAIK KHALEEFA	189Y1A0244	yes	Agree	Agree	Yes	5	5	Nil
38	SHAIK MULLA KHAJA MOINUDDIN	189Y1A0245	yes	Agree	Agree	Yes	4	5	Nil

39	SHAIK NAZEER BASHA	189Y1A0246	yes	Agree	Agree	Yes	5	5	Nil
40	SHAIK YOUSUF	189Y1A0247	yes	Agree	Agree	Yes	5	5	Nil
41	UPPALAPATI SURENDRA BABU	189Y1A0250	yes	Agree	Agree	Yes	5	5	Nil
42	VADDEMANI PAVAN KUMAR REDDY	189Y1A0251	yes	Agree	Agree	Yes	5	5	Nil
43	VEMA VENKATESH	189Y1A0252	yes	Agree	Agree	Yes	4	5	Nil
44	VEMA YOGESWARA	189Y1A0253	yes	Agree	Agree	Yes	5	5	Nil
45	BELLAGANTI DIVYASWINI (W)	199Y5A0201	yes	Agree	Agree	Yes	5	5	Nil
46	CHINTHAKUNTA GAYATHRI (W)	199Y5A0202	yes	Agree	Agree	Yes	5	5	Nil
47	GORANTLA BHUPATHI RAJU	199Y5A0203	yes	Agree	Agree	Yes	5	5	Nil
48	GUNDI NAGANNA	199Y5A0204	yes	Agree	Agree	Yes	4	5	Nil
48	KALAMALLA KALANDAR	199Y5A0205	yes	Agree	Agree	Yes	5	5	Nil
50	KAMMARA UDAYA SREE (W)	199Y5A0206	yes	Agree	Agree	Yes	5	5	Nil
51	MODAPOTHULA ANIL	199Y5A0207	yes	Agree	Agree	Yes	5	5	Nil


Coordinator

AS


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